Pregel: A System for Large-Scale Graph Processing

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Presented by: Ramya Nagarajan Spring 2019

Agenda

- Motivation
- Related Work
- Model of Computation
- Execution Architecture
- Experiments
- Final Remarks

Motivation

- Graph algorithms don't lend themselves to scalability and efficiency
 - $\circ \qquad \text{Poor locality of memory access}$
 - Changing degree of parallelism over course of execution
- No scalable system for arbitrary graph algorithms
- Need for scalable general-purpose system for executing graph algorithms in large-scale distributed environment

Related/Prior Work

- Existing distributed systems:
 - ie: MapReduce
 - Sub-optimal performance and usability
- Single-computer graph algorithm libraries
 - BGL, LEDA, NetworkX, JDSL, GraphBase, FGL
 - Not scalable
- Existing parallel graph systems
 - Parallel BGL, CGMgraph
 - $\circ \qquad \text{No fault tolerance} \qquad \qquad$
- Valiant's Bulk Synchronous Parallel Model

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- Output:
 - Set of vertex output values

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- User-defined handlers:
 - Specify behavior if message receiver does not exist in graph

• Combiners

- Function to combine result of all messages sent to a certain vertex
- Use by subclassing pre-defined Combiner class

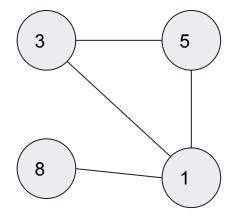
- Combiners
 - Function to combine result of all messages sent to a certain vertex
 - Use by subclassing pre-defined Combiner class
- Aggregators
 - Result made available to all vertices
 - Use by subclassing of pre-defined Aggregator class

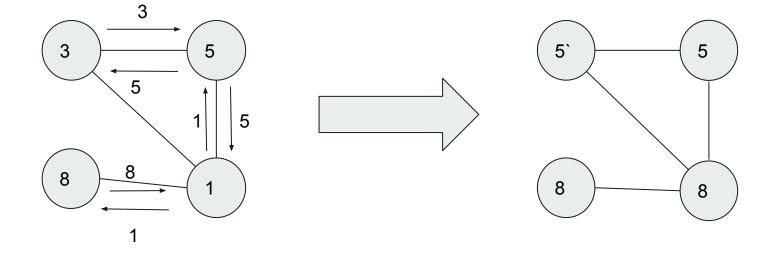
Topology Mutations

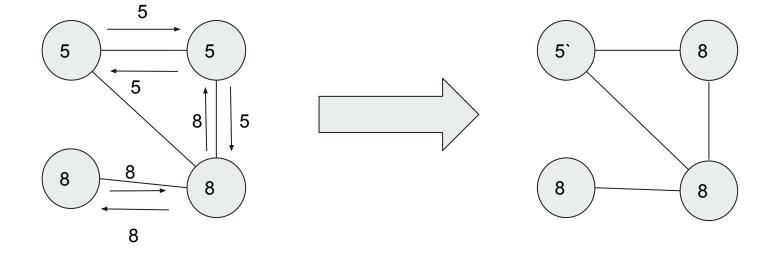
- Removals and additions
 - Add/remove edges
 - Add nodes
 - Remove nodes and outgoing edges

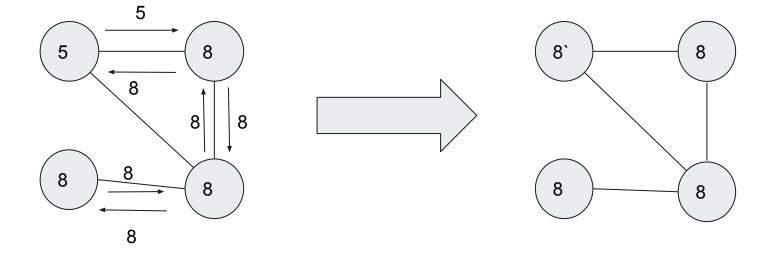
Topology Mutations

- Removals and additions
 - Add/remove edges
 - $\circ \quad \ \ \, \text{Add nodes}$
 - $\circ \qquad {\sf Remove nodes and outgoing edges}$
- Partial ordering
 - Removals before additions
 - Edge removals, vertex removals, vertex additions, edge additions
- Handlers









Applications

- PageRank
- Shortest Paths
- Bipartite Matching
- Semi-clustering

Example: Shortest Paths

- Implemented for single-source shortest path
- All vertices initialized to INF
- Superstep 0:
 - Source vertex updates value to 0, broadcasts to neighbors
- Subsequent supersteps:
 - Broadcast new minimum values
- Terminates when no remaining updates

Basic Architecture

- Graph partitioned into sets:
 - Each set contains a group of vertices + outgoing edges from vertices
- Worker and master machine separation

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- Execution Steps:
 - Many copies of the input graph run
 - Master determines number of partitions
 - Master assigns partitions to workers
 - Master orchestrates superstep start

Worker and Master Implementations

- Worker:
 - Loops through all vertices, performs Compute() step
 - Runs two threads:
 - Thread to process vertices
 - Thread to receive messages
- Master:
 - Coordinating worker activities
 - Barrier Synchronization

Fault Tolerance

- Checkpointing
 - Worker state saved
 - Frequency selected based on mean time to failure model
- "Ping" messages for failure detection
- Confined recovery

Experiments

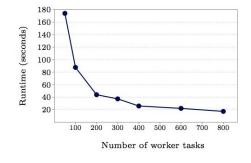


Figure 7: SSSP—1 billion vertex binary tree: varying number of worker tasks scheduled on 300 multicore machines

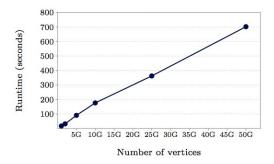


Figure 8: SSSP—binary trees: varying graph sizes on 800 worker tasks scheduled on 300 multicore machines

Experiments

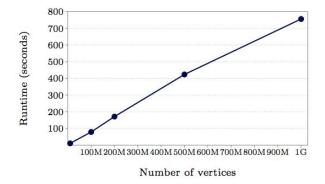


Figure 9: SSSP—log-normal random graphs, mean out-degree 127.1 (thus over 127 billion edges in the largest case): varying graph sizes on 800 worker tasks scheduled on 300 multicore machines

Final Remarks

- Fault tolerant, scalable implementation of model
- "Think like a vertex" vs "Think like a graph"
 - Improved locality
 - Improved linear scalability
- Model for many other graph processing algorithms (ie Apache Giraph)